

FIG. 1

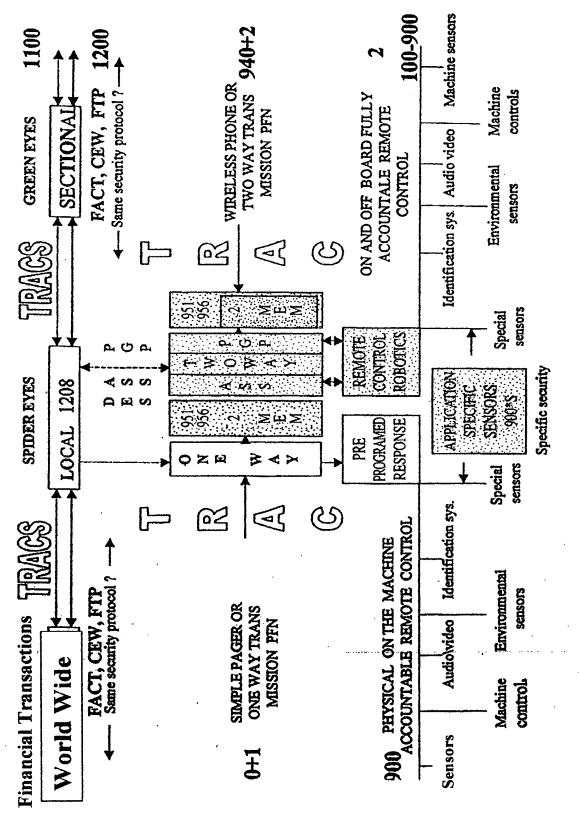


FIG. 21

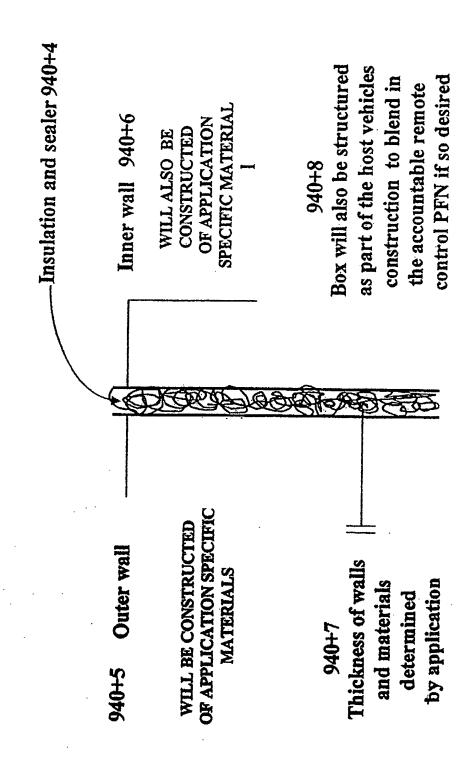


FIG. 20

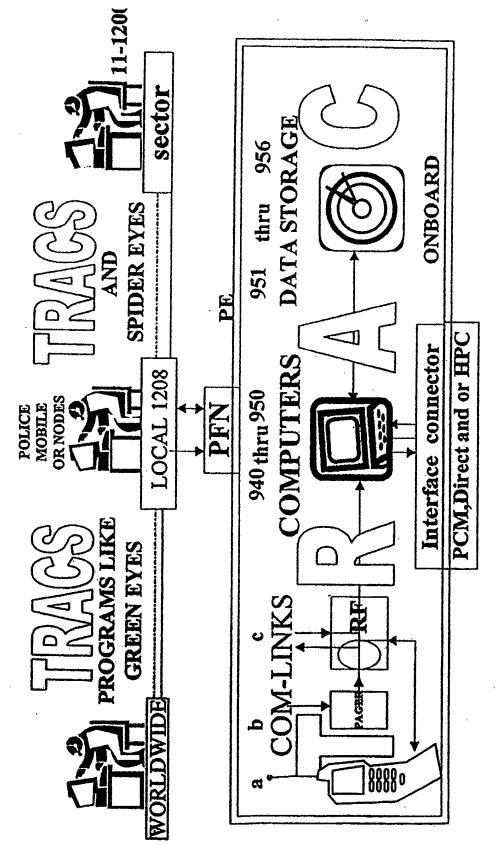
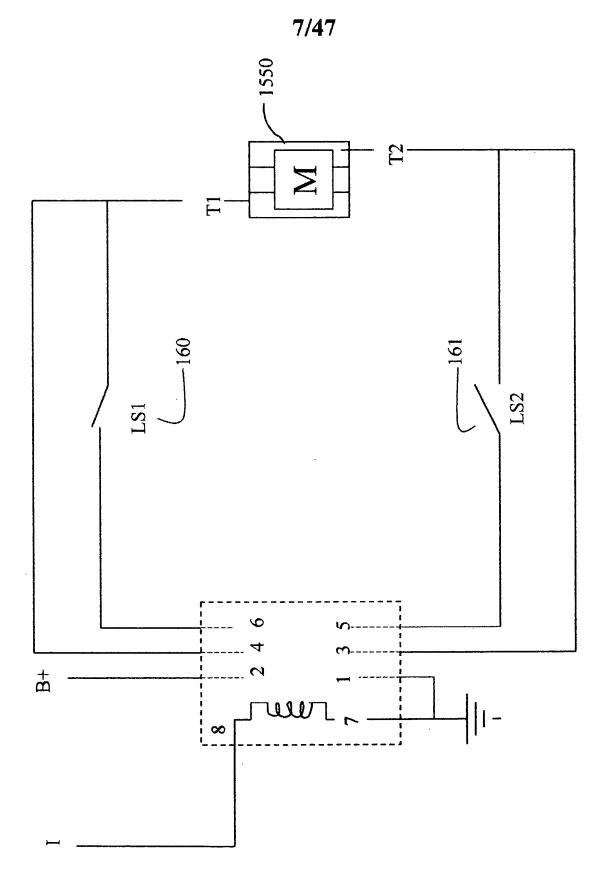


FIG. 2D

init: x var word	if in 12=1 and solenoid=1 and tflag=1 then slowdown check4: go to start	
tflag var bit relay var bit	checkt:	tflag=0
solenoid var bit switch var bit	for x=1 to 35 pause 100	hold1: if in 14=1 then hold 1 goto check 2
tflag=0	if in 14=0 then nogo	
relay=0 switch=0	next goto go	carrun: out1=0
solenoid=0	nogo: goto check1	out2=0 out3=0
input 12 input 14	go: tflag=1	solenoid=1
input 15 output 4	triag=1 debug "go condition",cn	goto check 3
outout l	goto check1	slowdown:
output 2 output 3	noactivity:	if switch=0 then fir if switch=1 then sec
output 5	debug "Beeper is Inactive", cr	fir: out1=1 accelerator diseng.
output 6 output 7		siren-flasher
output 8	relayc: debug "relay control"	out8=0 play message out9=1 amp on for play
output 9 output 10	debug? relay if relay=0 then first	out9=1 amp on for play pause 15000
output 11 output 13	if relay=0 then first if relay=1 then second	out8=1 off record chip out 10=0 reset recorder chip
out]=0	first:	pause 1000
out2=0 out3=0	debug "first" out4=1	out10=1 reset ready pause 21000 out2=1 progressive brake
out <u>4</u> =0	out2=1 out3=Pause 2,300 RS out4=0	out2=1 progressive brake
out5=0 out6=0	relay=1	application switch=1
out7=0 out8=1	goto relend second:	goto swend sec:
out9=0	debug "second"	out3=1 energize kill
out10=1 out11=0	out2=0 out5=1	out5=1 pause 3000
out13=0	pause 3000 out5=0	out5=0 pause 45000
start: if in 15=1 then init reset ID sys.	relay=0	out9=0
if in 14-1 then checkt check1:	tilag=0 hold: if in 14=1 then	switch=0 tflag=0
if tflag=1 and in 12=0 then relaye	hold	hold3: if in 14=1 then hold
chečk2: if in 12=1 and solenoid=0 then carrun	stucki; ii in 13=0 then	3 stuck2: if in 15=0 then
check3:	goto init	stuck 2
	relend:	goto init swend:
		· rs



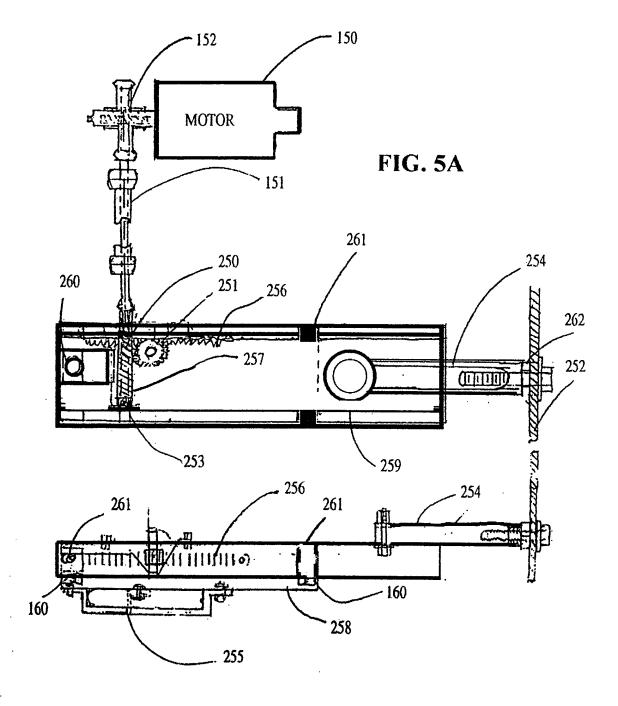
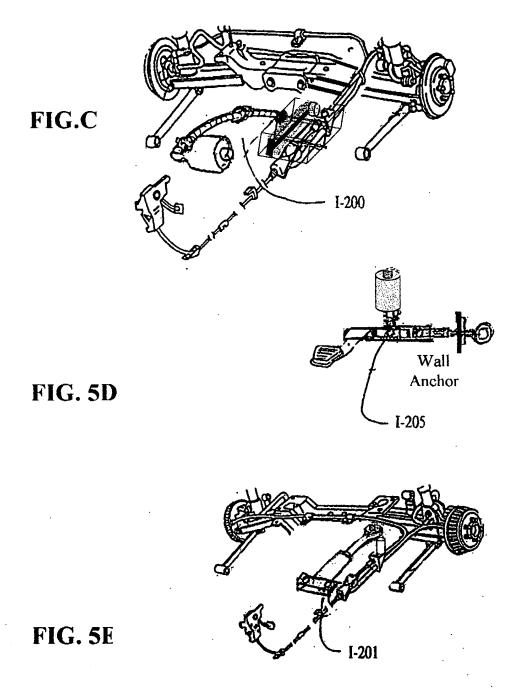


FIG. 5B



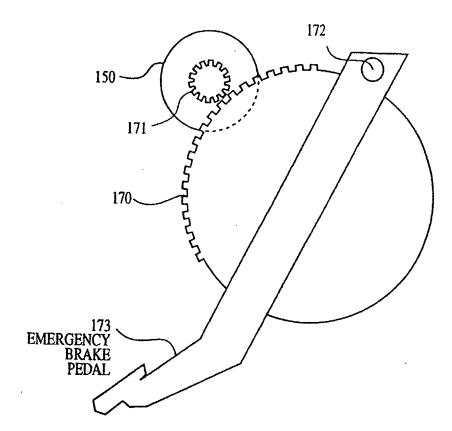
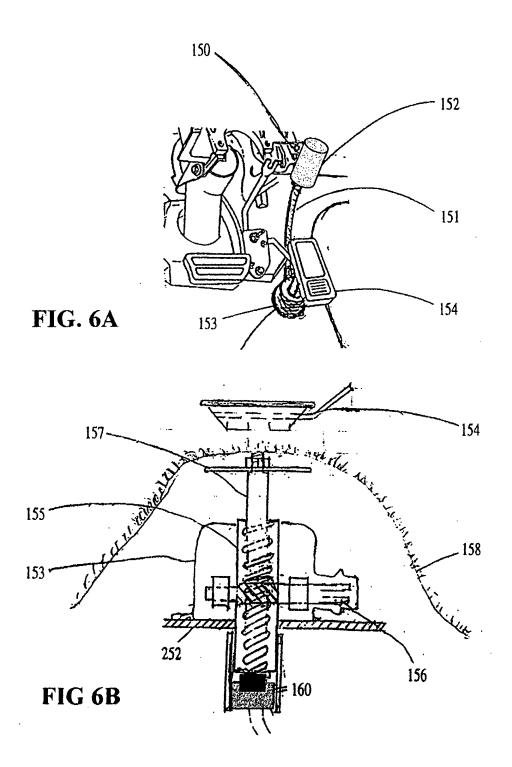
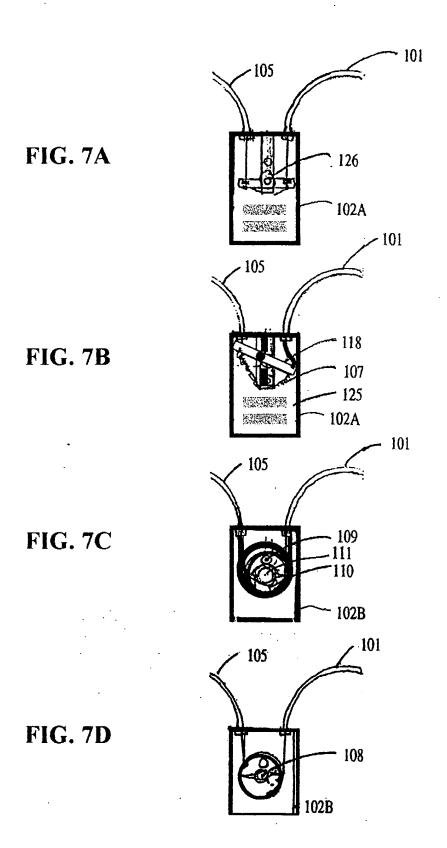
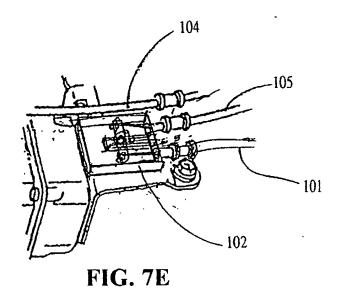


FIG. 5F

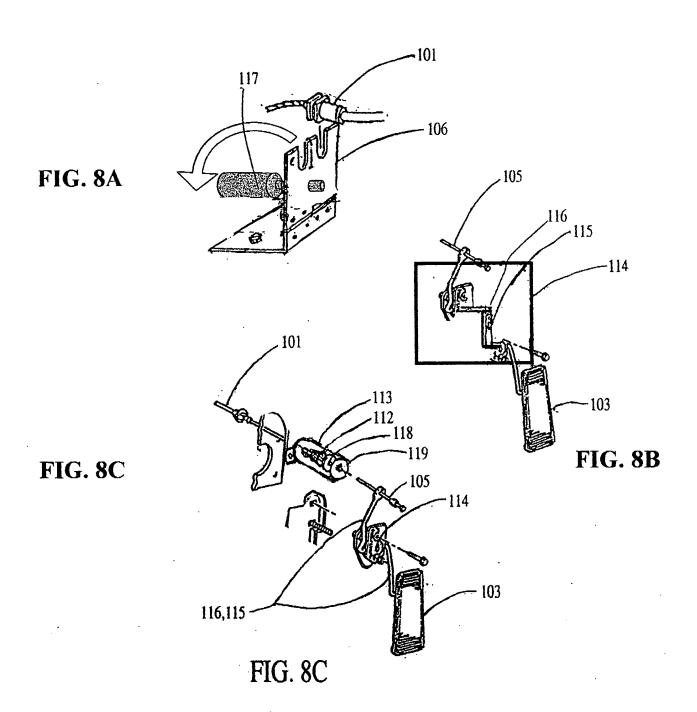




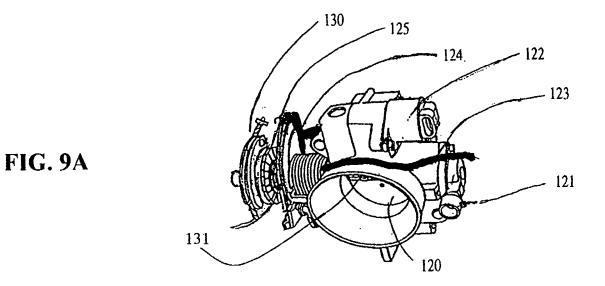


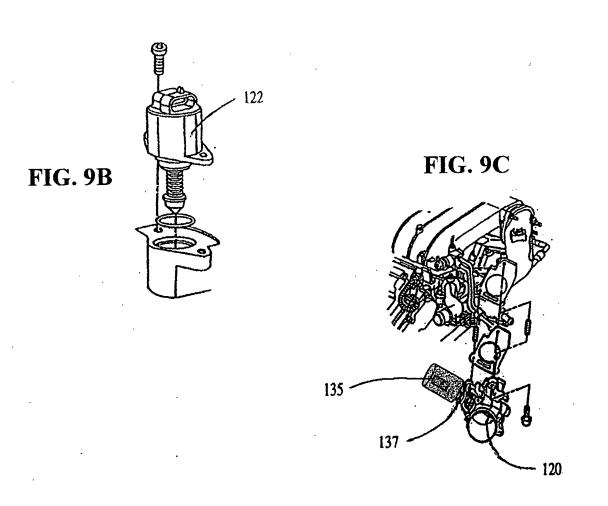
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FIG. 7F



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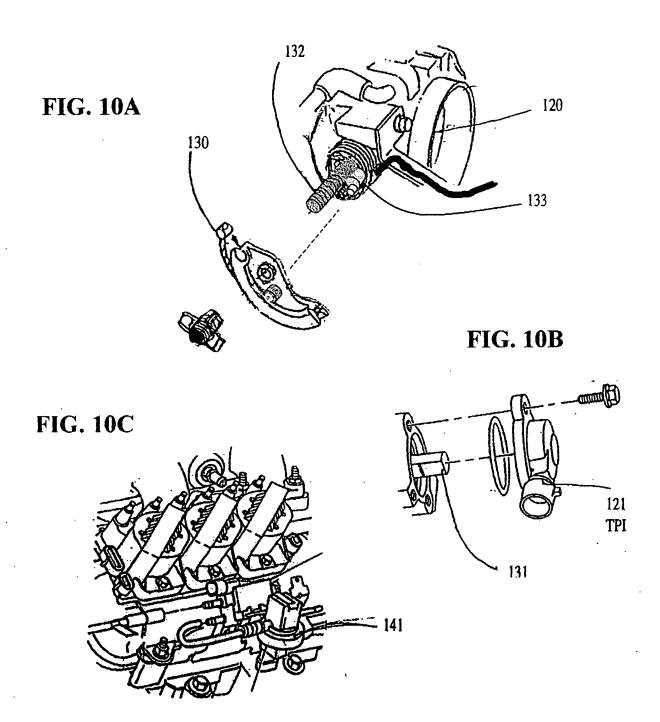


FIG. 11A

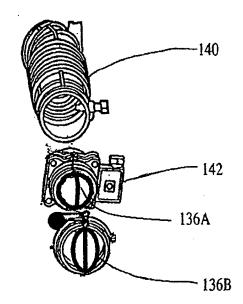
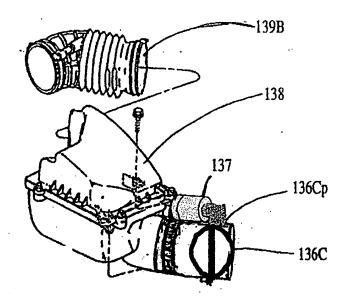
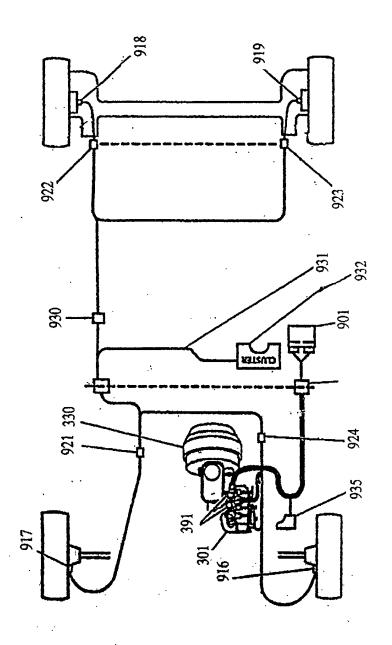


FIG. 11B





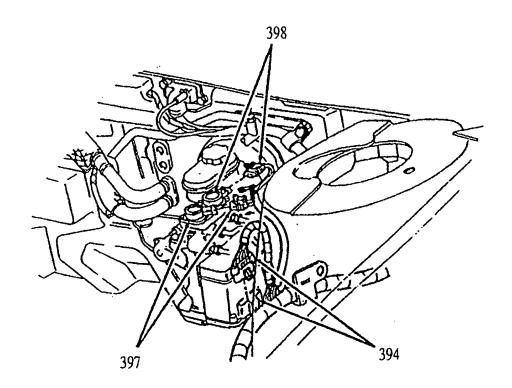
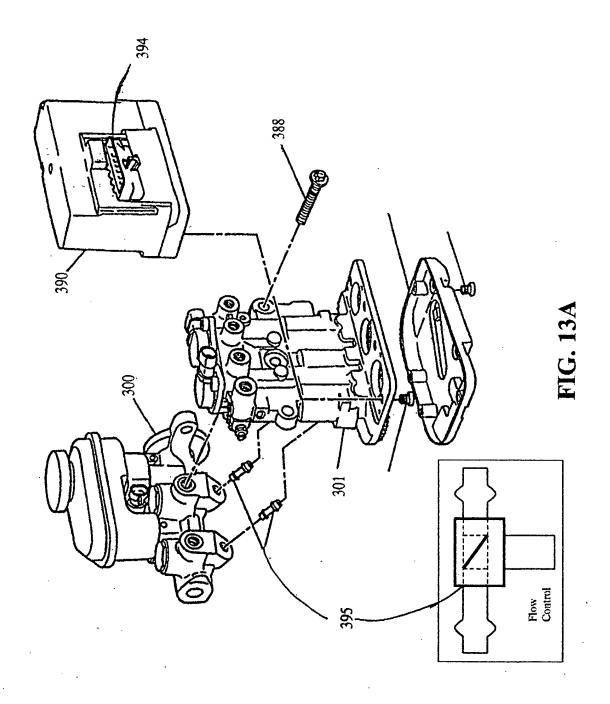


FIG. 12B



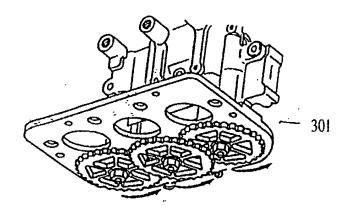
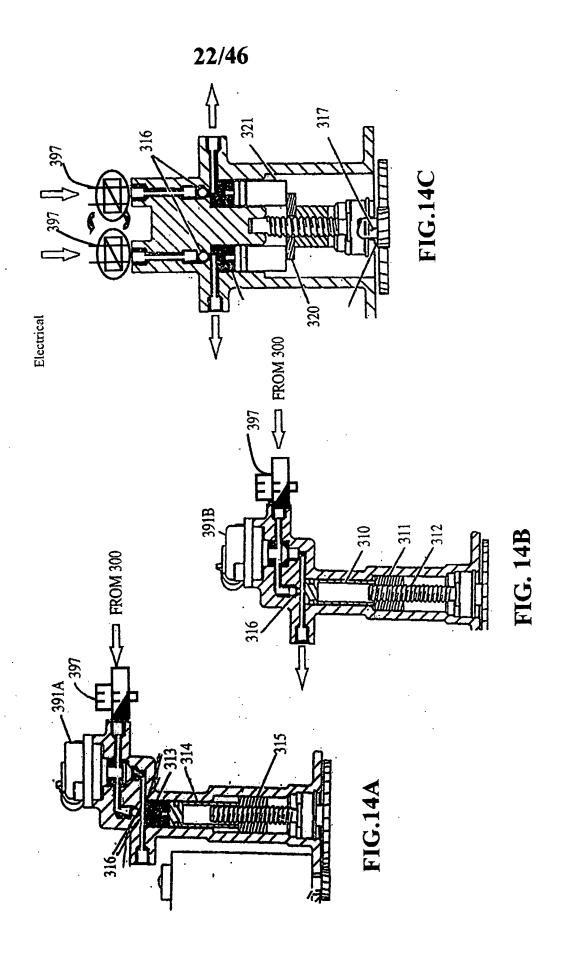


FIG. 13B



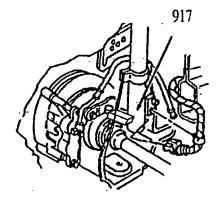


FIG.14D

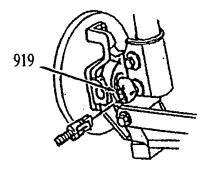


FIG. 14E

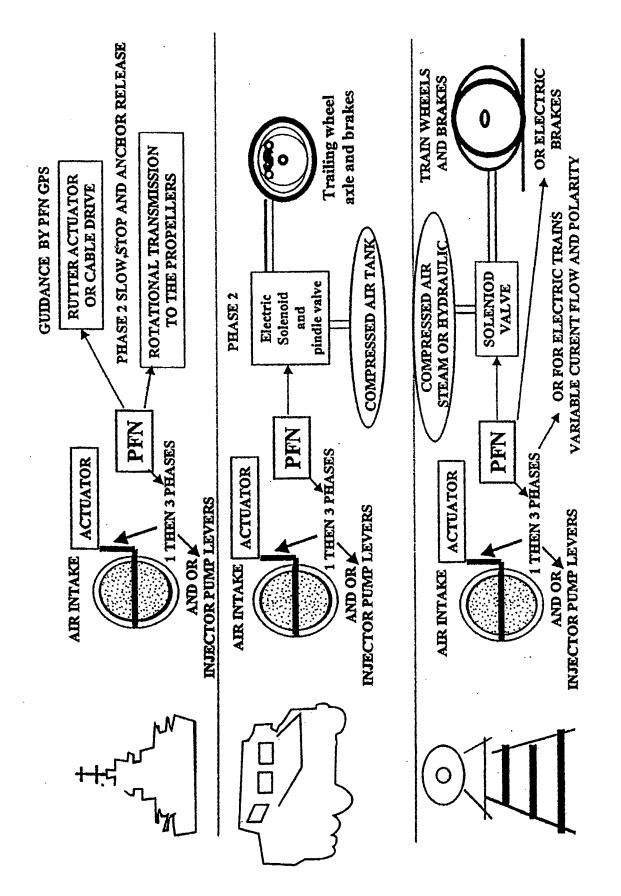


FIG. 14F

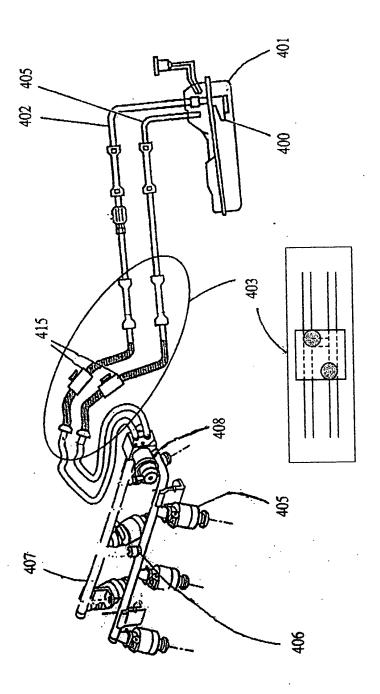


FIG. 15A

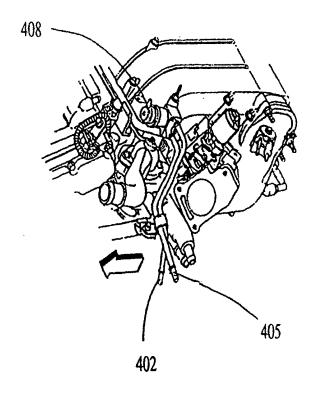


FIG. 15B

FIG. 16A

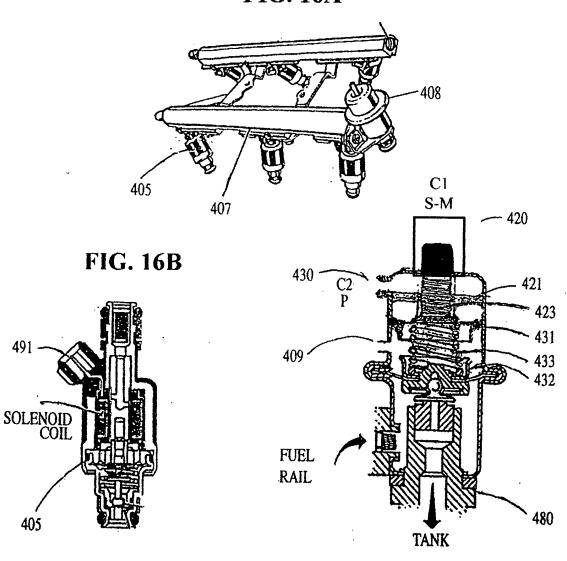
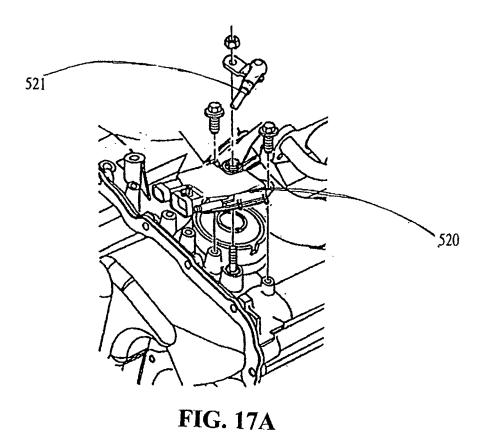
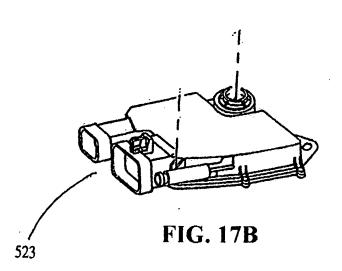
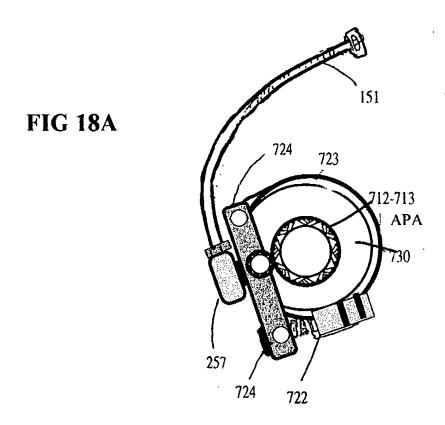
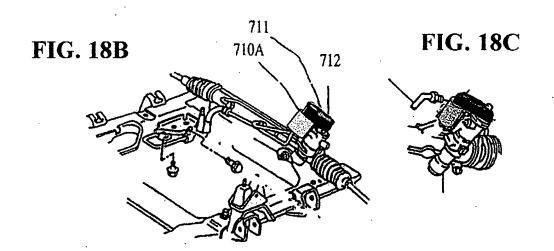


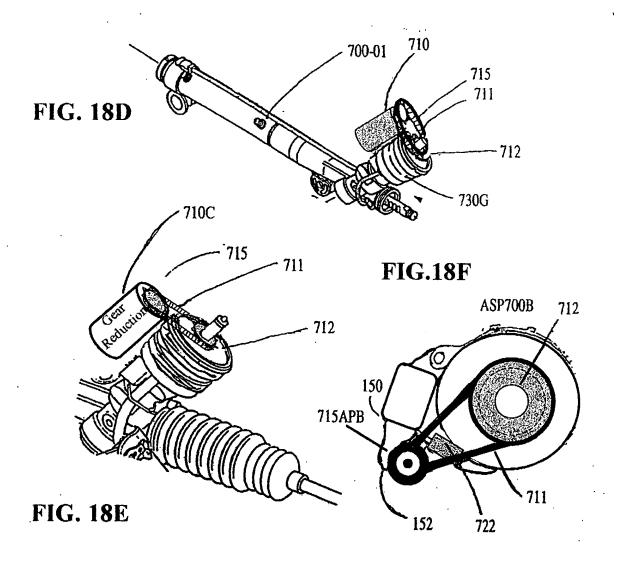
FIG. 16C

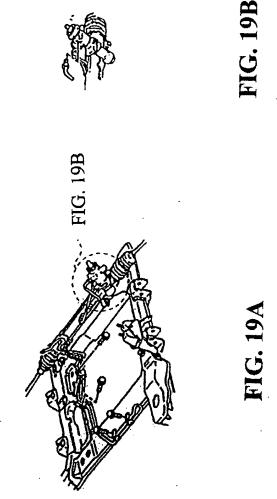












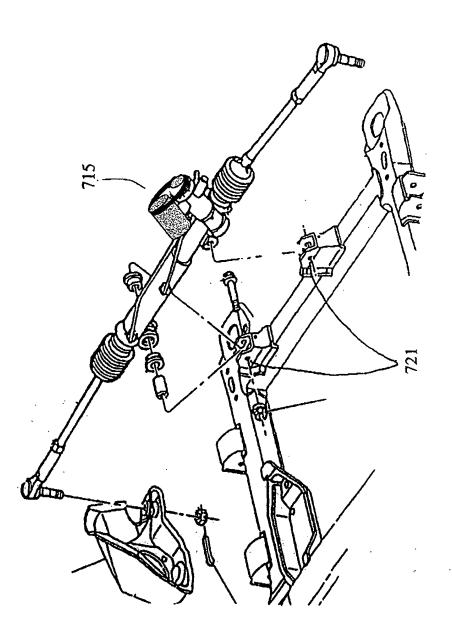


FIG. 19C

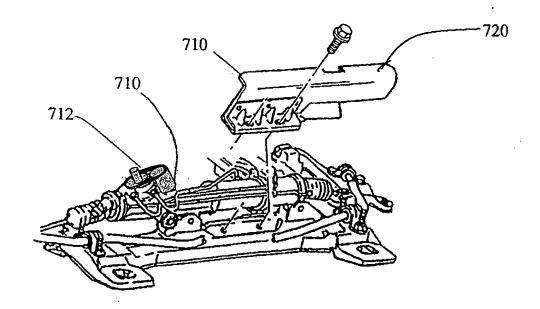
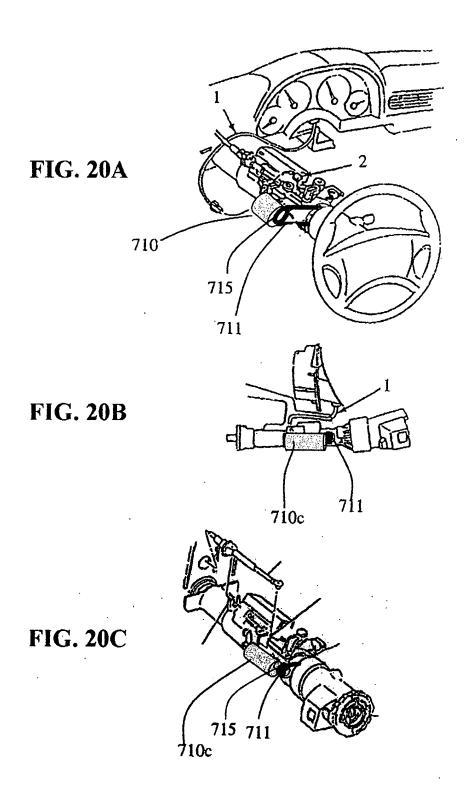
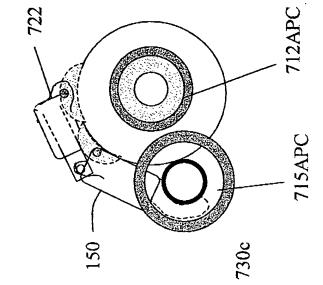


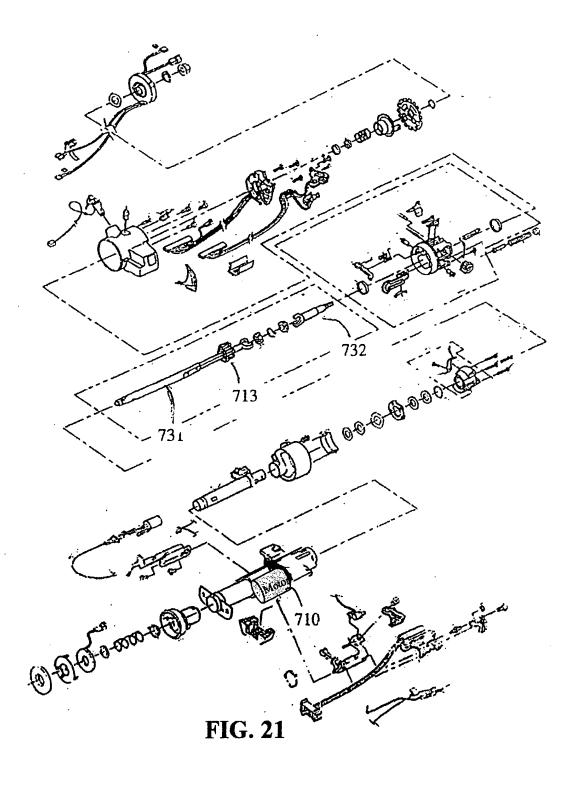
FIG. 19D

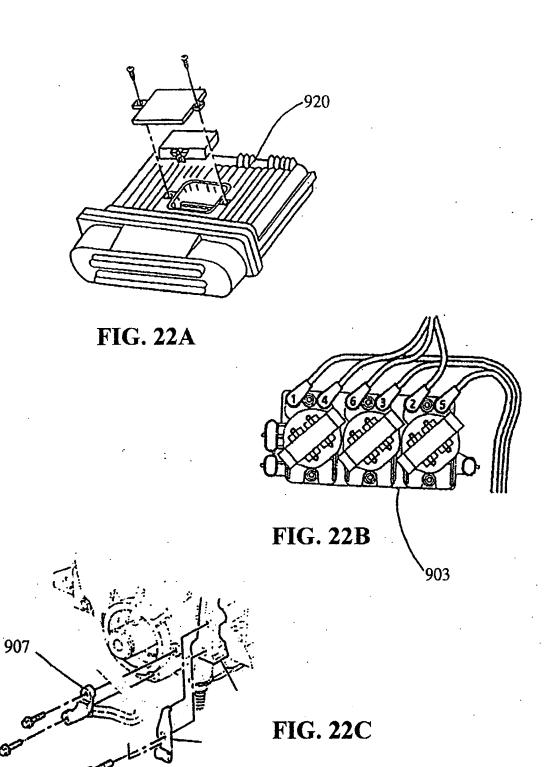




SP700Rc

COC OTA





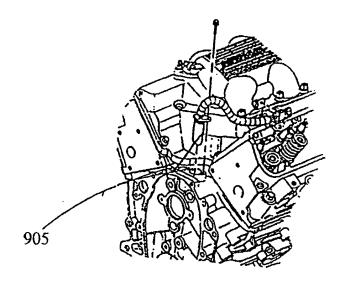


FIG. 23A

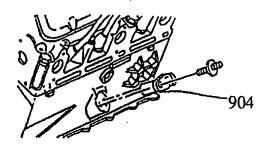


FIG. 23 B

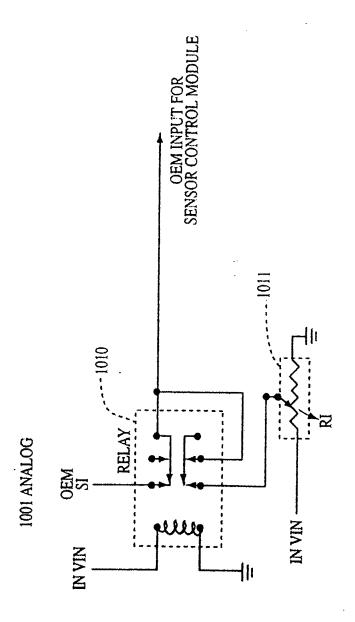


FIG. 24A

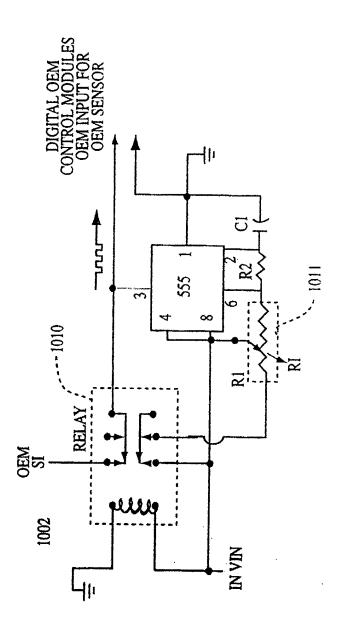
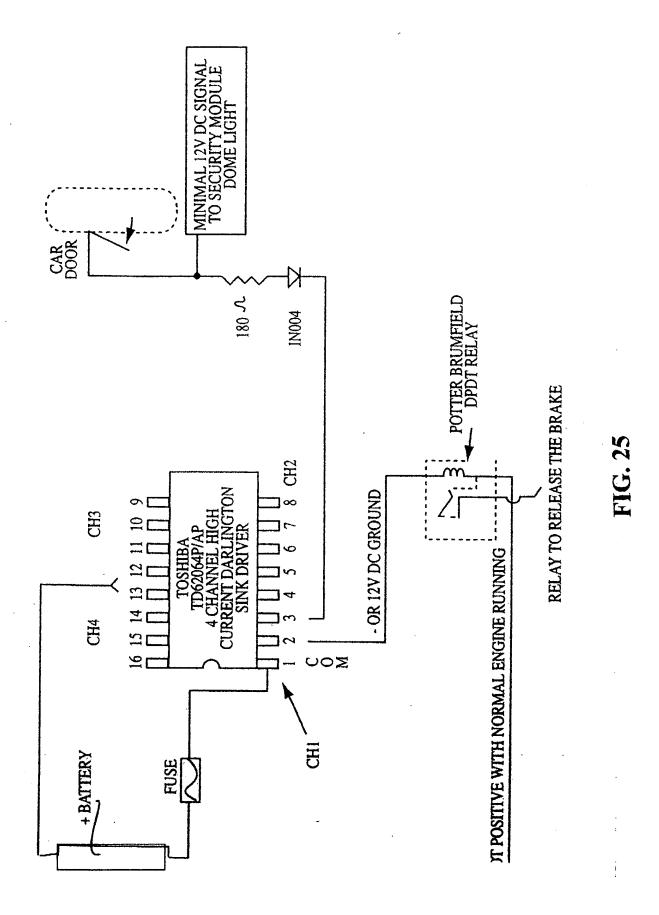
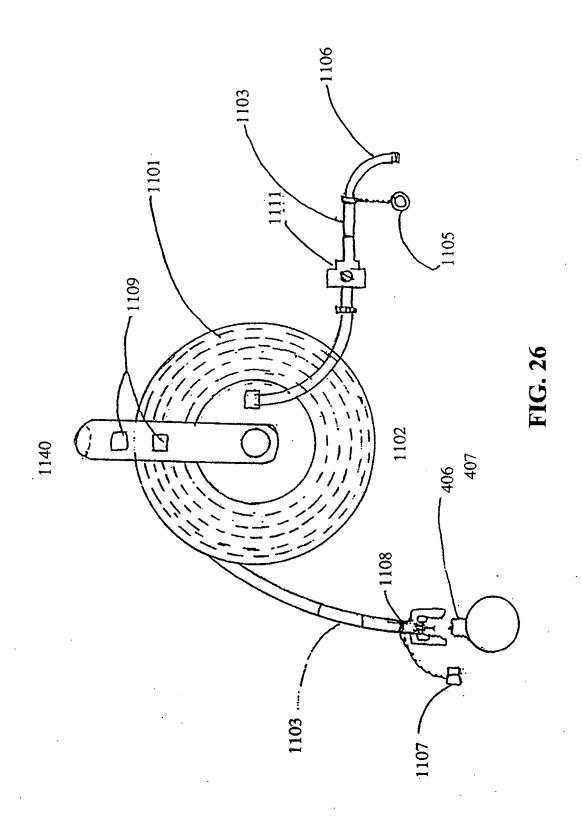


FIG. 24E





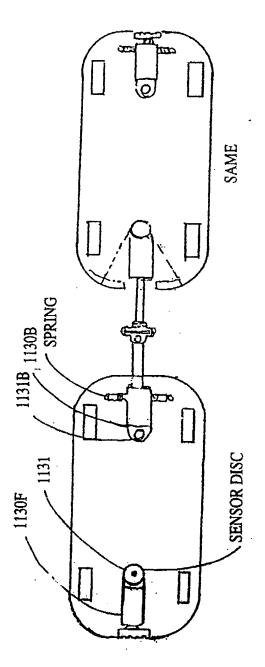


FIG. 2′

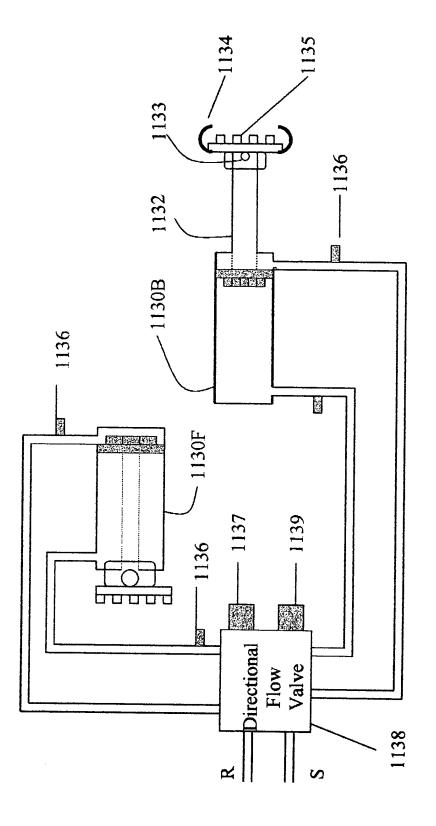


FIG. 28

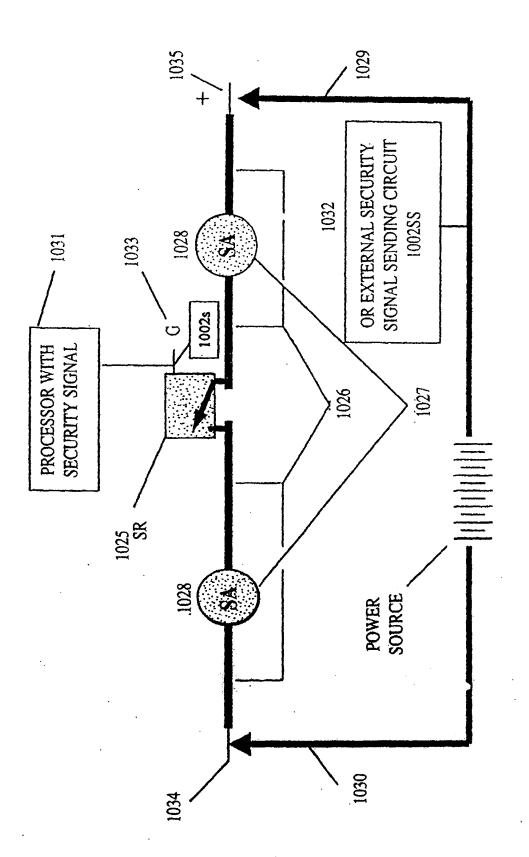


FIG. 2

